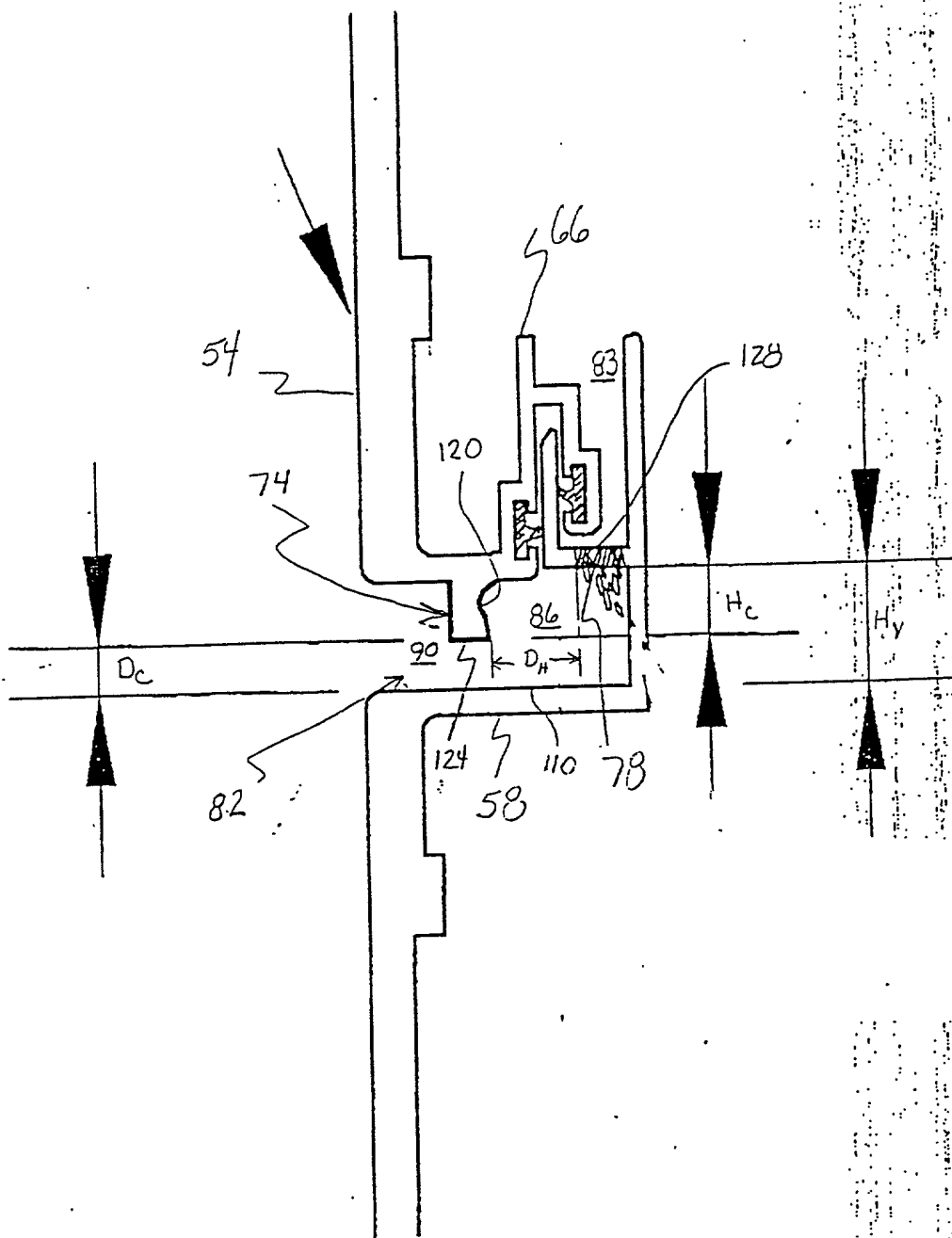
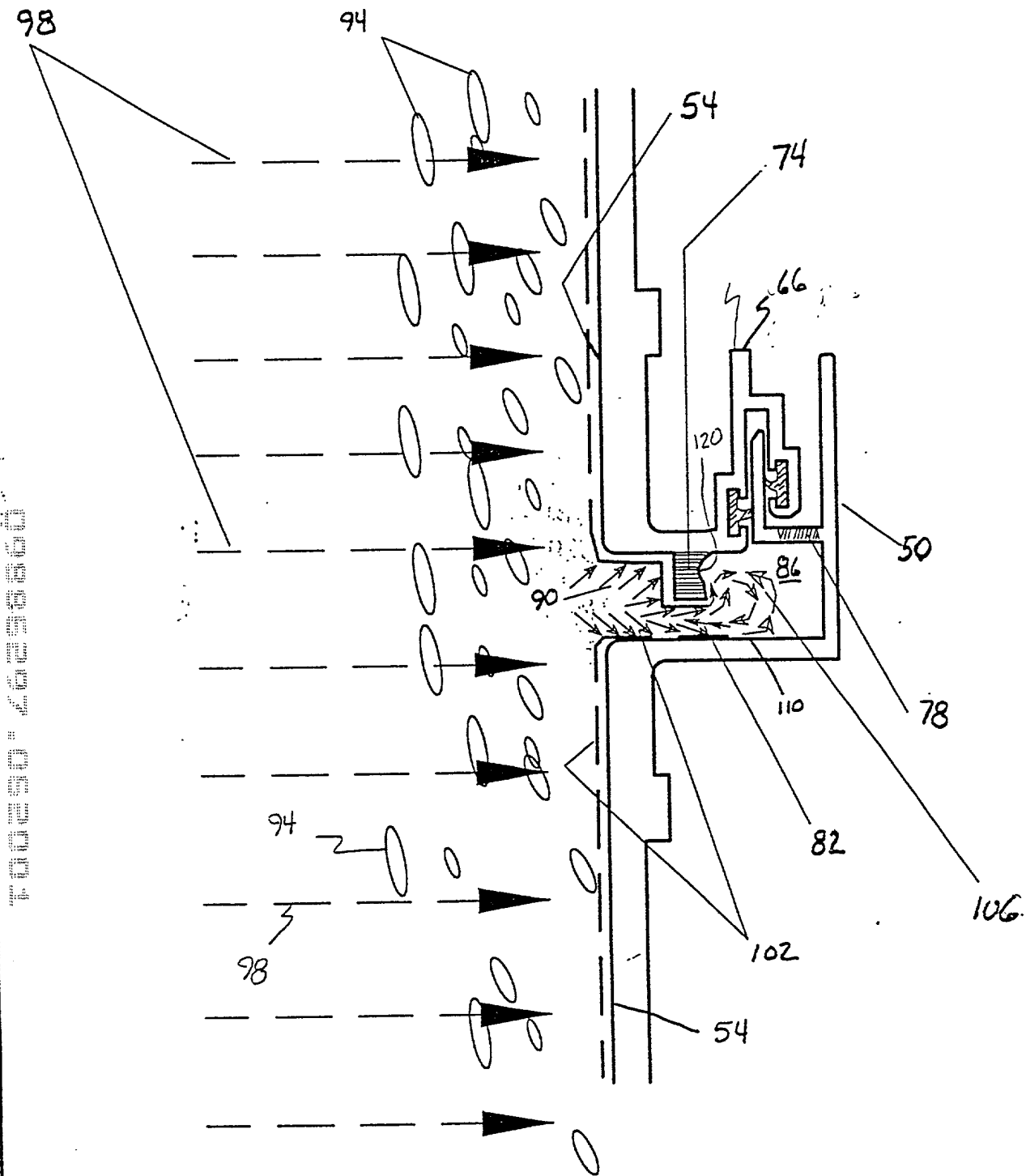


FIG. 2



(2)

FIG. 2



(3)

FIG. 3

1.0330 2.0330 3.0330 4.0330 5.0330 6.0330 7.0330 8.0330 9.0330 10.0330 11.0330 12.0330 13.0330 14.0330 15.0330 16.0330 17.0330 18.0330 19.0330 20.0330 21.0330 22.0330 23.0330 24.0330 25.0330 26.0330 27.0330 28.0330 29.0330 30.0330 31.0330 32.0330 33.0330 34.0330 35.0330 36.0330 37.0330 38.0330 39.0330 40.0330 41.0330 42.0330 43.0330 44.0330 45.0330 46.0330 47.0330 48.0330 49.0330 50.0330 51.0330 52.0330 53.0330 54.0330 55.0330 56.0330 57.0330 58.0330 59.0330 60.0330 61.0330 62.0330 63.0330 64.0330 65.0330 66.0330 67.0330 68.0330 69.0330 70.0330 71.0330 72.0330 73.0330 74.0330 75.0330 76.0330 77.0330 78.0330 79.0330 80.0330 81.0330 82.0330 83.0330 84.0330 85.0330 86.0330 87.0330 88.0330 89.0330 90.0330 91.0330 92.0330 93.0330 94.0330 95.0330 96.0330 97.0330 98.0330 99.0330 100.0330

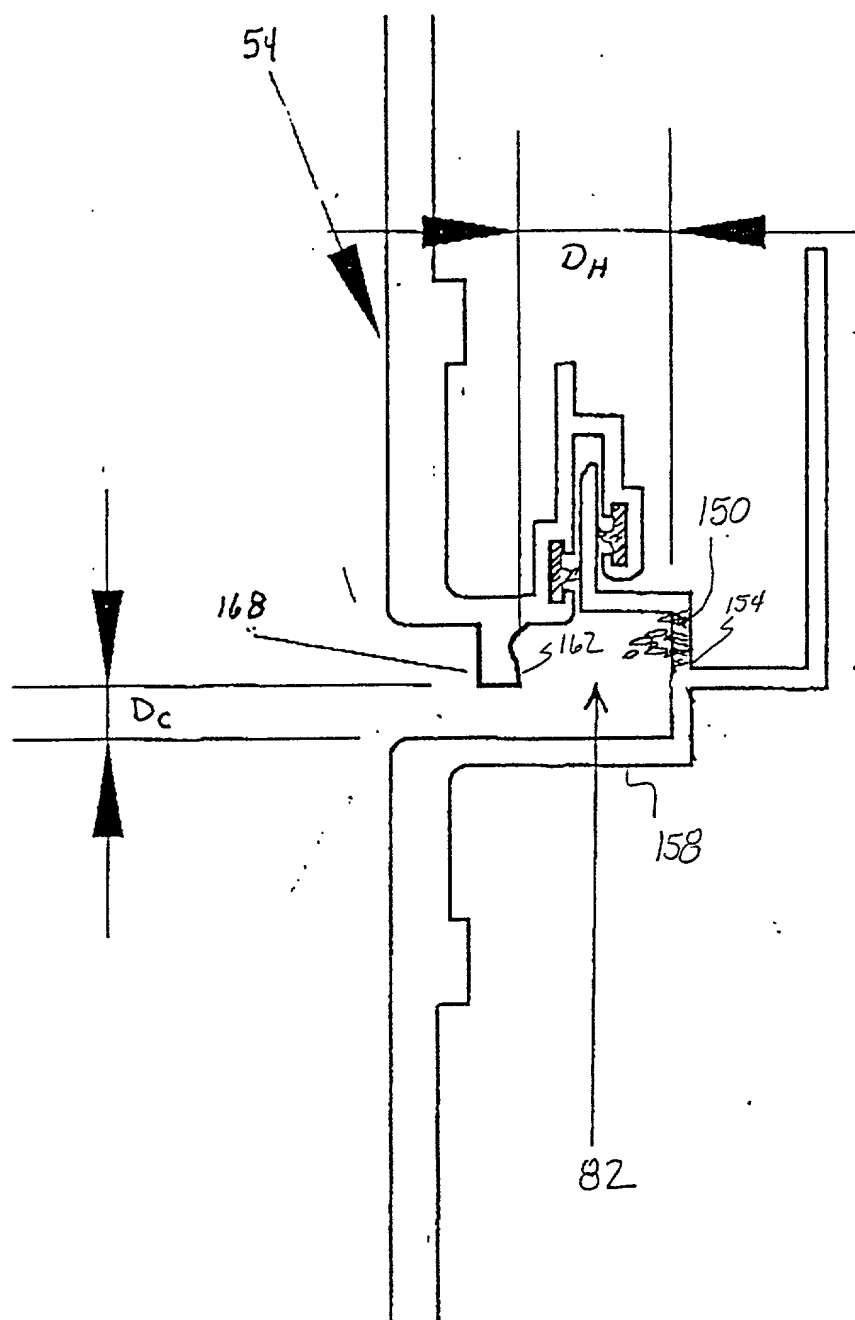


Fig. 4.

A schematic diagram of a semiconductor device layout. The diagram shows a central structure 200, which is a complex, multi-layered rectangular block. To the left of structure 200 is a region labeled 208, which is a rectangular area. To the right of structure 200 is a region labeled 212, which is a rectangular area. Below structure 200 is a region labeled 216, which is a rectangular area. Above structure 200 is a region labeled 218, which is a rectangular area. The diagram also includes several dimensions: D_H is the horizontal distance between the left and right edges of the central structure 200; D_C is the vertical distance between the top and bottom edges of the central structure 200. Arrows indicate the direction of the dimensions D_H and D_C . The diagram is a top-down view of the device layout.

Fig. 5

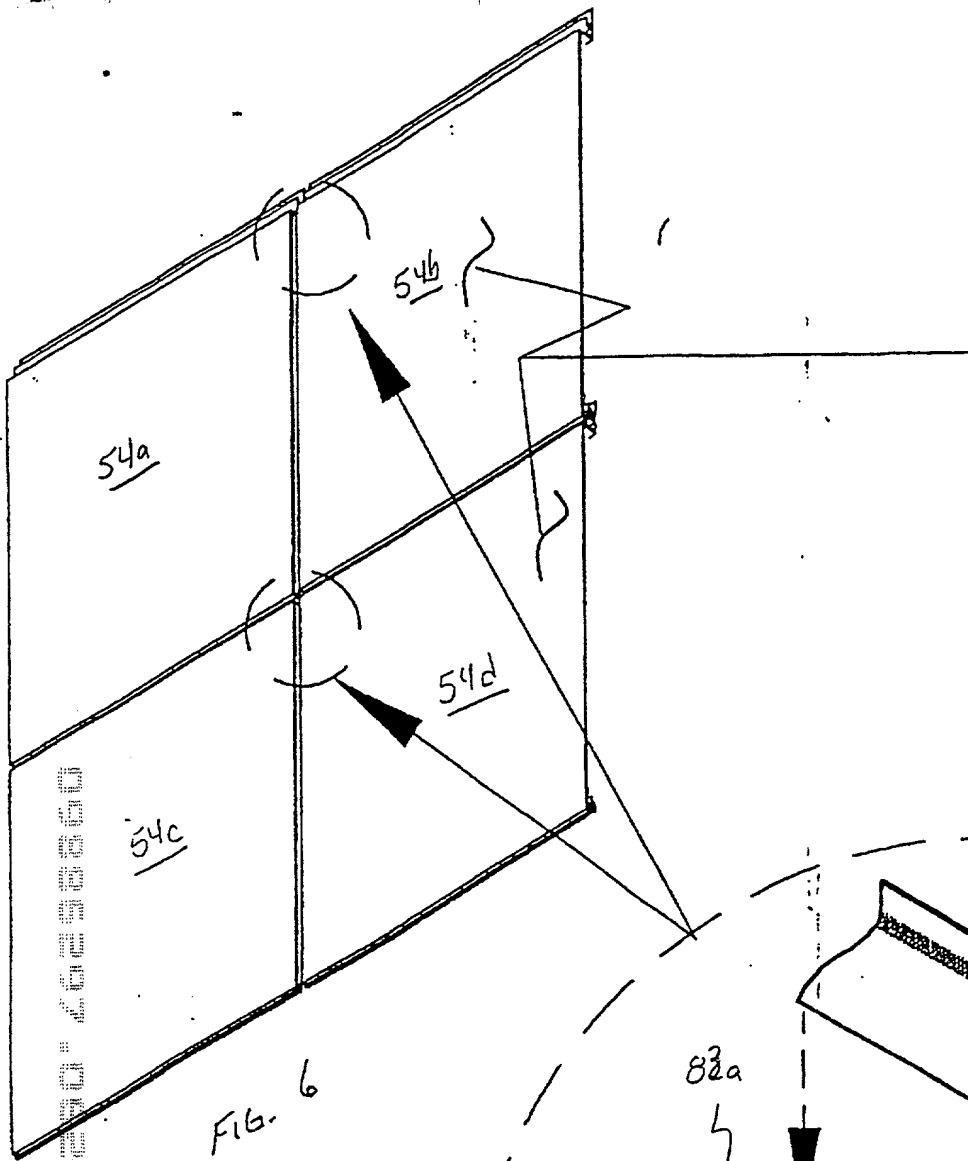


FIG. 6

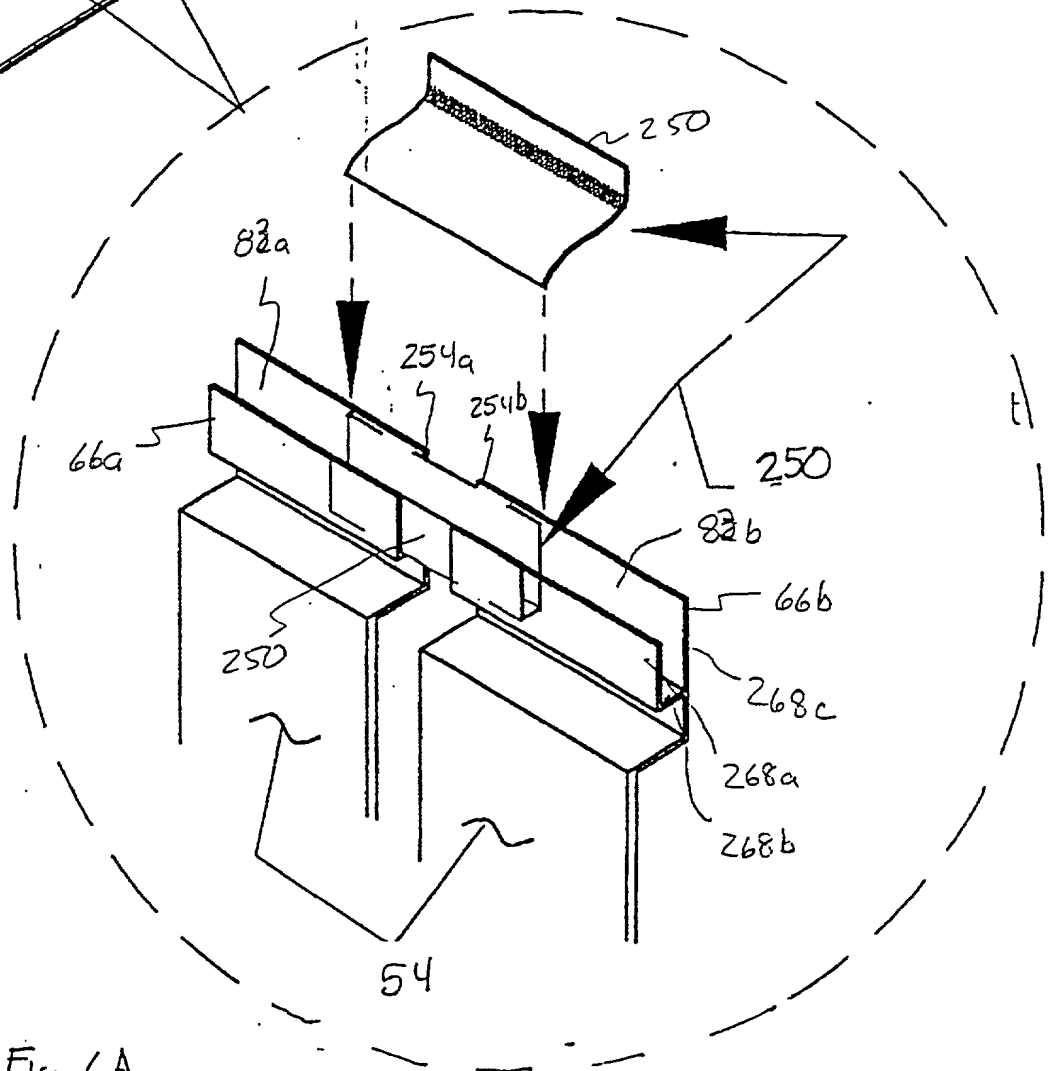
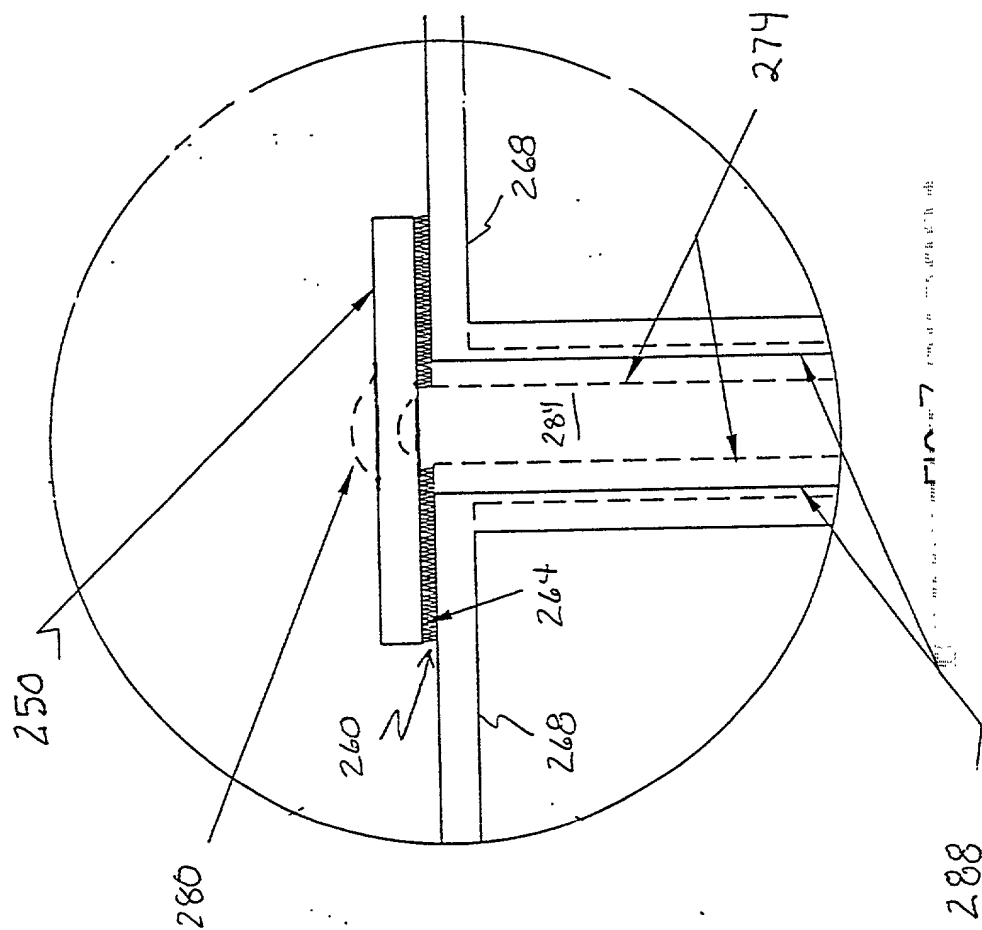
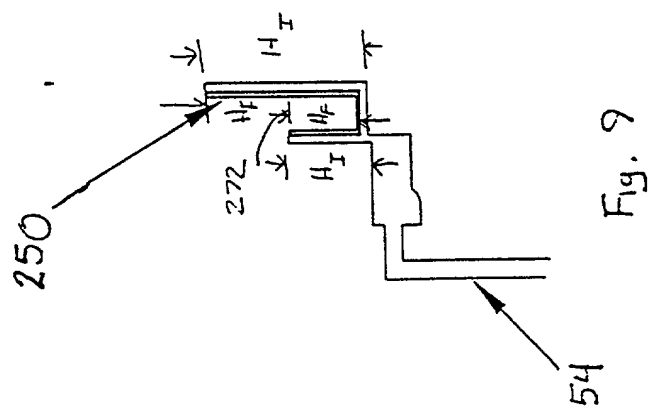
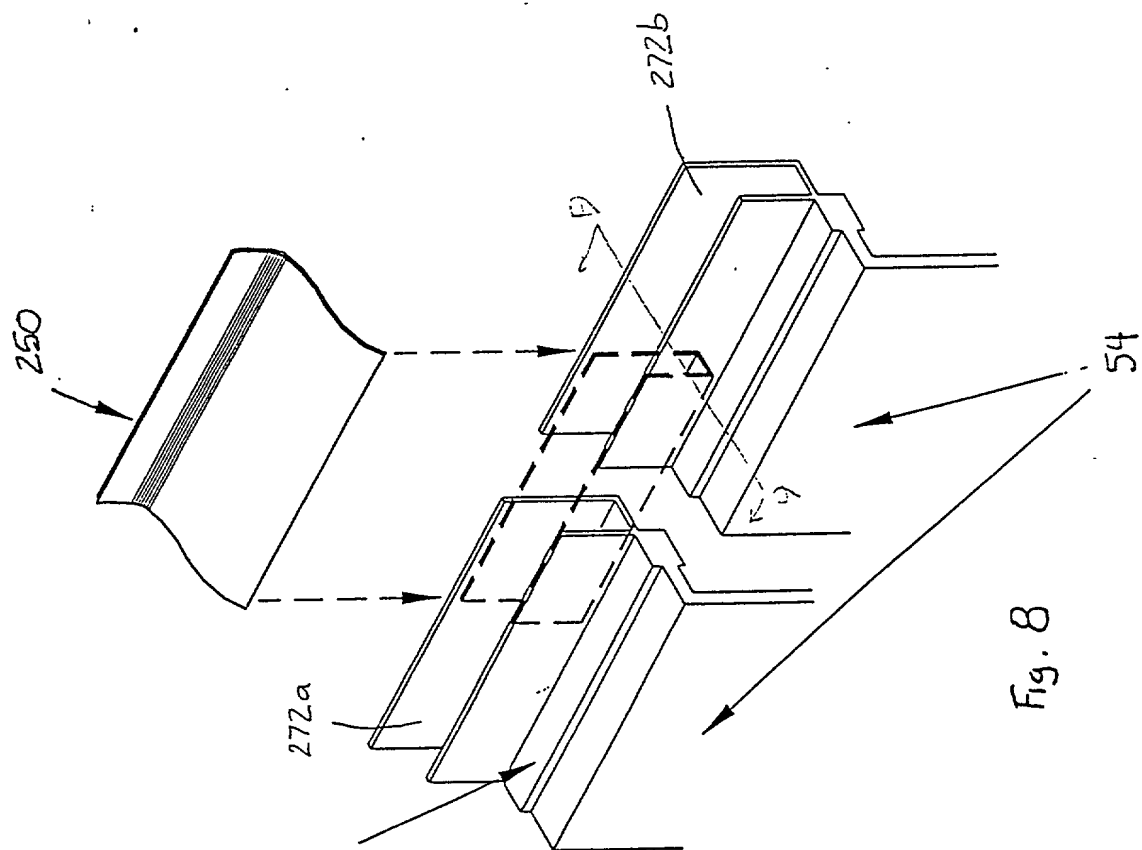


FIG. 6A





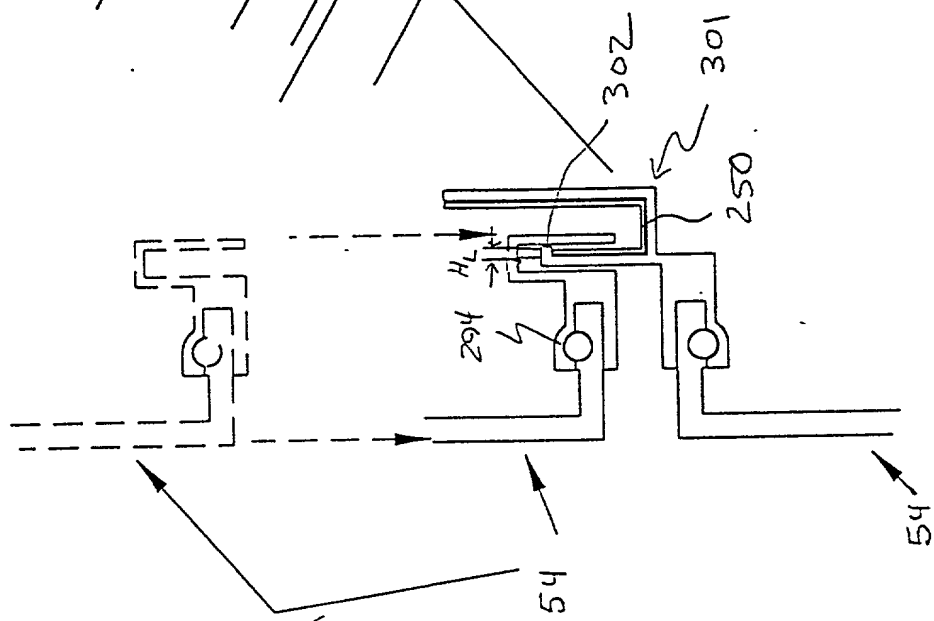


FIG. 12

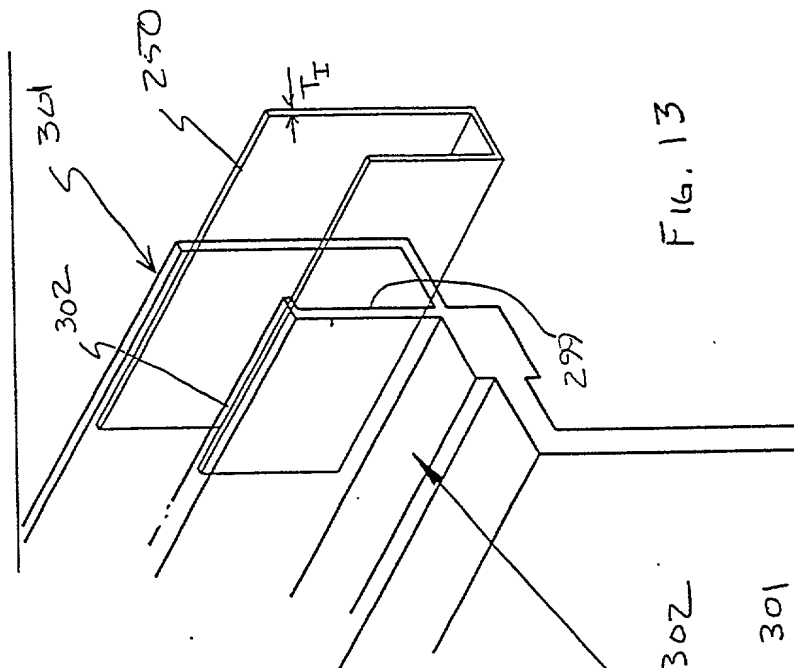


FIG. 13

FIG. 14

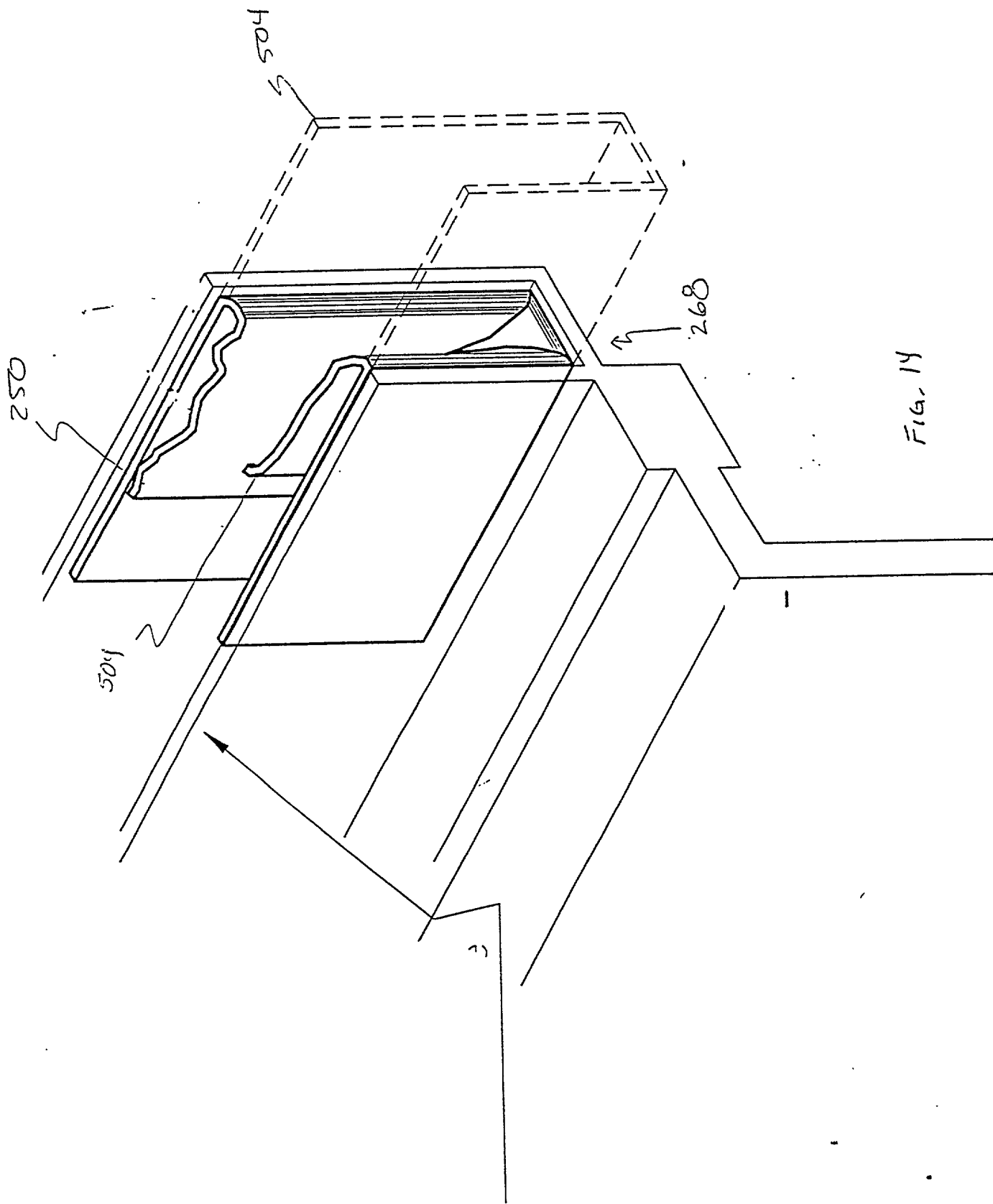


FIG. 14

[illegible]

Fig. 15

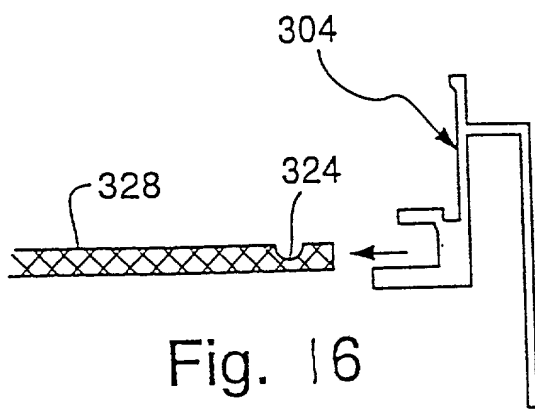


Fig. 16

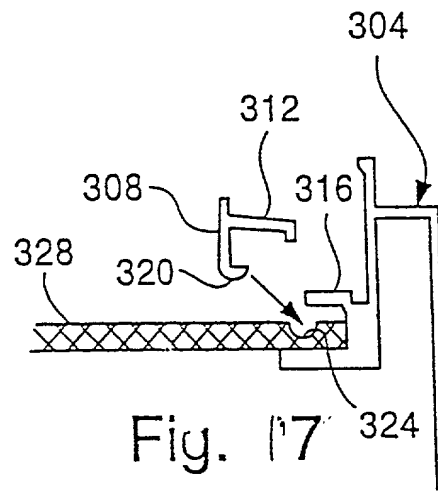


Fig. 17

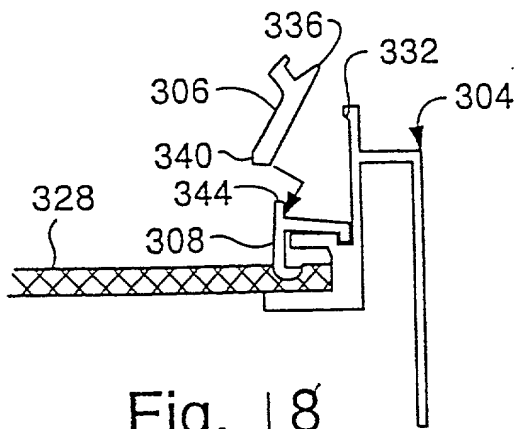


Fig. 18

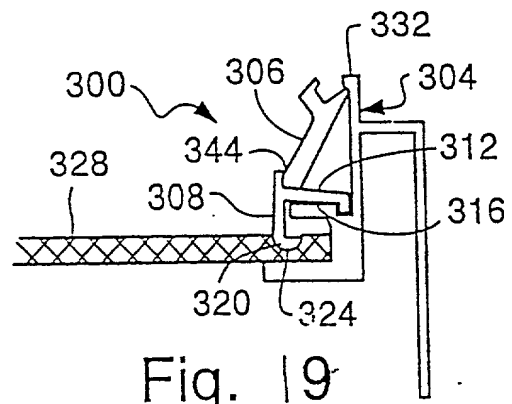


Fig. 19

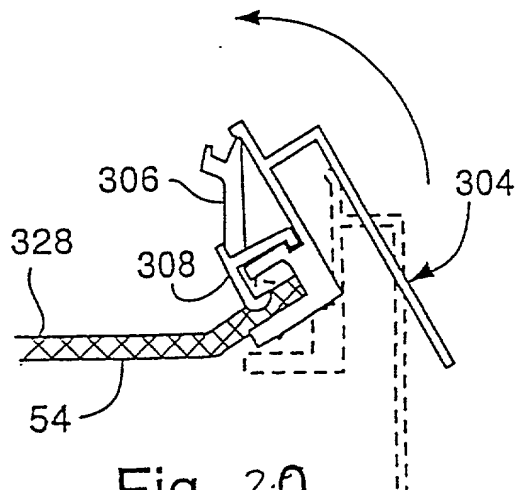


Fig. 20

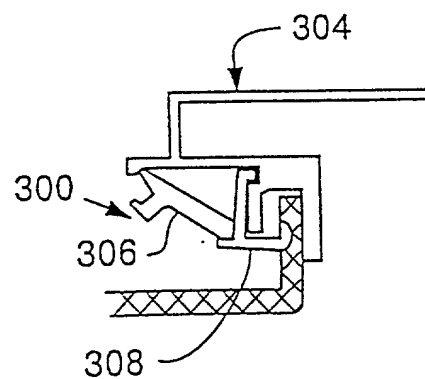
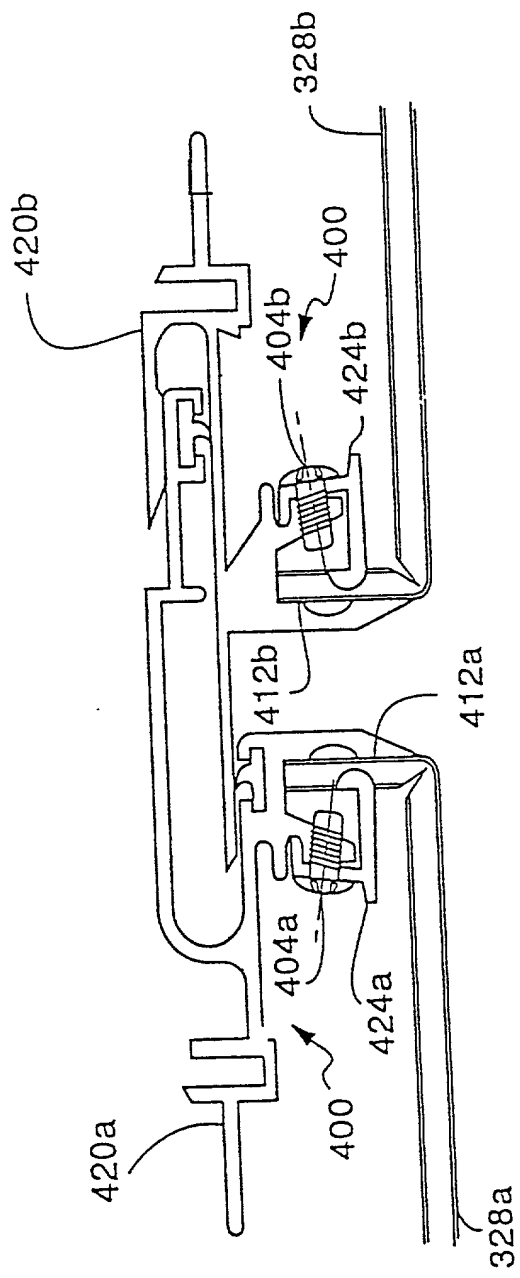


Fig. 21

Fig. 22



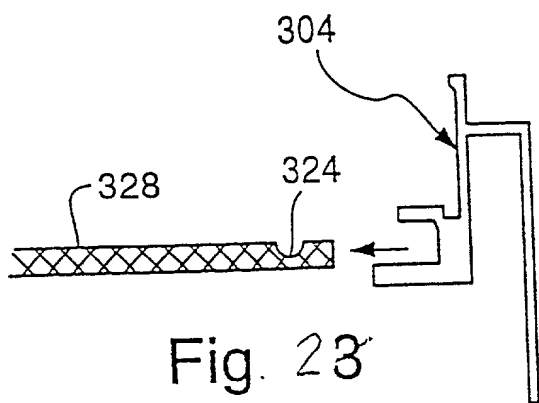


Fig. 23

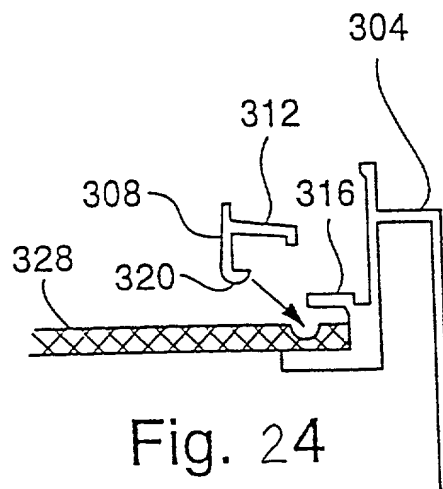


Fig. 24

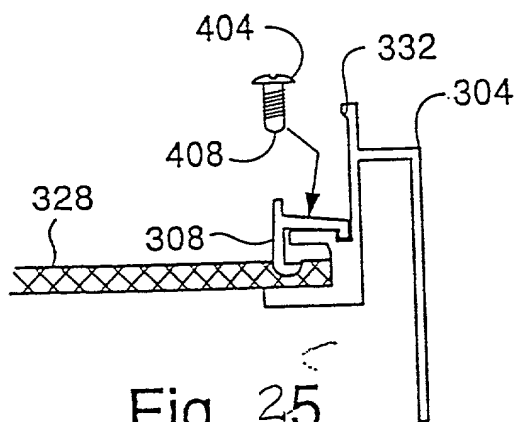


Fig. 25

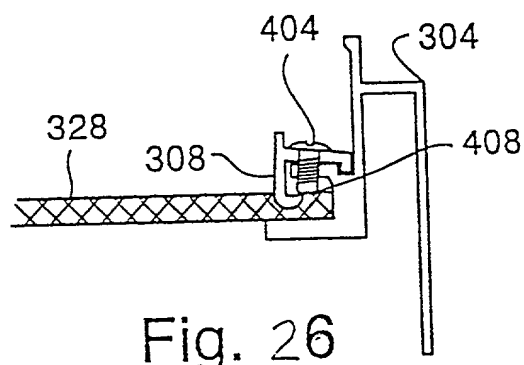


Fig. 26

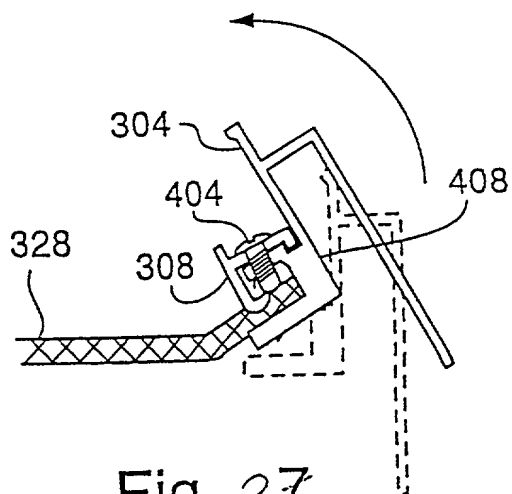


Fig. 27

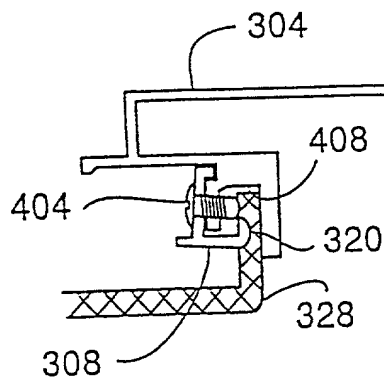


Fig. 28